

**VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING & TECHNOLOGY**

(An Autonomous Institution)

Bachupally, Nizampet(S.O), Hyderabad-90

**Department of Electrical and Electronics Engineering**

**Summary Sheet of Intellectual Property Rights (IPRs)**

<b>Particulars</b>	<b>Prior to accreditation visit</b>	<b>After the accreditation visit</b>
<b>Intellectual Property Rights (IPRs)</b>	<b>04+02 (Under review)</b>	<b>07 + 02 (Response submitted to first examiner report)</b>

**Prepared by**

**Verified by**

**Approved by**

**Dr. Naresh Pasula**

**File In-Charge**

**HOD**

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**Department of Electrical and Electronics Engineering**

## Intellectual property rights (IPRs) – Patents/Books/Book chapters

### After the accreditation visit

S.No	Name of the faculty member/s	Intellectual Property Rights AY_2018-19		
		Title of the Patent	File Number	Status of the Patents/Books/Book chapters
1.	Dr. M. Ramamoorthy, Dr. G. S. Raju, Dr. K. Anuradha, Dr. Ravi Kumar	A Passive Filter Configuration to Reduce THD Produced by Non-Linear Loads	2029/CHE/2011 dated 21-12-2012	First report received on 25-05-18 and compliance to examiners report is submitted on 25-02-19. The present status in Amended stage.
2.	Dr. M. Ramamoorthy, Dr. G. S. Raju, Dr. K. Anuradha, Mr. G. Sasi Kumar, Mr. A. A. Rajeshwar	A DC-To-DC Converter Configuration by Soft Switching Devices	2028/CHE/2011 dated 21-12-2012	First report received on 29-05-18 and compliance to examiners report is submitted on 28-02-19. The present status in Amended stage.

S.No	Name of the faculty member/s	Intellectual Property Rights AY_2017-18		
		Title of the Patent	File Number	Status of the Patents/Books/Book chapters
1.	Dr. D. Ravi Kumar and Dr. N. Krishna	Torque Ripple Minimization of a FOC-Fed PMSM with MRAS Using Popov's Hyper-Stability Criterion (Book Chapter)	ISBN: 978-981-10-4852-4_12	Published
2.	Dr. J.Srinivasa Rao	Design and Simulation of Boost Converter for Correction of Power Factor and THD Reduction (Book Chapter)	ISBN: 978-981-10-7329-8_37	Published
3.	Dr. K. Anuradha and Dr. J. Viswanadha Rao	IEEPS-18	ISBN: 978-93-8759-301-5 (e-book)	Published

S.No	Name of the faculty member/s	Intellectual Property Rights AY_2016-17		
		Title of the Patent	File Number	Status of the Patents/Books/Book chapters
1.	Dr. D. Ravi Kumar	Optimal Operation of an Integrated Power Distribution System Fed with Renewable Energy Sources, Diesel Generation and Battery Storage (Book Chapter)	ISBN: 978-981-10-1540-3	Published
2.	Dr.T.Nireekshana	Deregulated Power System with Special Emphasis on ATC-Generation Reschedule and FACTS Devices	ISBN: 978-3-330-06111-8	Published

S.No	Name of the faculty member/s	Intellectual Property Rights AY_2015-16		
		Title of the Patent	File Number	Status of the Patents/Books/Book chapters
	NIL			

## Prior to the accreditation visit

S.No	Name of the faculty member/s	Intellectual Property Rights AY_2014-15		
		Title of the Patent	File Number	Status of the Patents/Books/Book chapters
	NIL			

S.No	Name of the faculty member/s	Intellectual Property Rights AY_2013-14		
		Title of the Patent	File Number	Status of the Patents/Books/Book chapters
	NIL			

S.No	Name of the faculty member/s	Intellectual Property Rights AY_2012-13		
		Title of the Patent	File Number	Status of the Patents/Books/Book chapters
1.	G.Lakshminarayana	Microcontroller Based DC Motors	ISBN: 978-3-659-16851-2	Published

2.	Dr. Venu Yarlagadda	Implementation of Control Circuit of 1-Ph SVC Using Microcontroller (Text Book)	ISBN: 978-3-659-14697-8	Published
		Improvement of system stability margins using coordination control of Static Var Compensator (SVC) and Thyristor Controlled Series Capacitor (TCSC), Proceedings of the Third International Conference on Trends in Information Telecommunication and Computing (Book Chapter)	ISBN: 978-1-4614-3363-7	Published
		Power system generator and voltage stability enhancement by the hardware circuit implementation of 3-Ph static var compensator (SVC), Mobile Communication and Power Engineering (Book Chapter)	ISBN: 978-3-642-35864-7	Published