

VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING & TECHNOLOGY

(An Autonomous Institution)

Bachupally, Nizampet(S.O), Hyderabad-90

Department of Electrical and Electronics Engineering

**Summary Sheet of Funded Research and Development (R &D)
Projects and Consultancy**

Particulars	Prior to accreditation visit	After the accreditation visit
Funded and consultant Amount in Lakhs	33.45	55.91

Prepared by

Verified by

Approved by

Dr. Naresh Pasula

File In-Charge

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VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING & TECHNOLOGY

(An Autonomous Institution)

Bachupally, Nizampet(S.O), Hyderabad-90

Department of Electrical and Electronics Engineering

Funded R & D projects

After the accreditation visit

S.No	Name of the faculty member/s	Funded R&D Projects AY-2018-19				Status of the project report
		Name of the Agency	Title of the Project	File Number	Amount Sanctioned	
1.	Dr. Naresh Pasula and Mr. D.S.G. Krishna	TIASN, SEED, DST	Development of Low Cost-Efficient Charging Station for Electric Vehicle (EV) Charging Applications	SEED/TIASN/2018 /46	10.3 Lakhs	Received mail from DST that proposal recommended for funding
		MSME	Design and manufacture of autonomous ventilated seats for electric and IC engine based vehicles	-	10 Lakhs	Submitted
2.	Dr. K. Sudharani and Dt. Nireekshana	IMPRESS, ICSSR	Sensor based milk Adulteration Device	P3841	9.4 Lakhs	Submitted
3.	Mr. P. Ramesh	VNR VJIET	Solar hybrid processing units for small scale entrepreneurs	Office note (Jan-2017)	1 Lakh	On-going
4.	Dr. E. Shiva Prasad	MSME	Development of storage independent PV based water pumps for Agriculture applications	-	6.1 Lakhs	Submitted

5.	Dr. K. Anuradha and Mr. G. C. Prabhakar	AICTE	Data acquisition based control design of regular and special electrical machines under MODROBS	9-109/RIFD/MODR OB/Policy-1/2016-17	12 Lakhs	On-going
6.	Mr. D.S.G. Krishna	FIST, DST	Development of Advanced Digital Signal Processing & Drives	SR/FST/College-148/2013(C)	11.1 Lakhs	On-going

S.No	Name of the faculty member/s	Funded R&D Projects AY-2017-18				
		Name of the Agency	Title of the Project	File Number	Amount Sanctioned	Status of the project report
1.	Mr. D.S.G. Krishna	FIST, DST	Development of Advanced Digital Signal Processing & Drives	SR/FST/College-148/2013(C)	11.1 Lakhs	On-going
2.	Mr. P. Ramesh	VNR VJIET	Solar hybrid processing units for small scale entrepreneurs	Office note (Jan-2017)	1 Lakh	On-going
3.	Dr. K. Anuradha and Mr. G.C. Prabhakar	AICTE	Data acquisition based control design of regular and special electrical machines under MODROBS	9-109/RIFD/MODR OB/Policy-1/2016-17	12 Lakhs	On-going
4.	Dr. K. Anuradha and Dr. J. Viswanadha Rao	AICTE	International conference on innovations in Electronics, Power, Smart Grids & advanced computing Technologies (IEEPS-2017)	67-44/RIFD/GOCPOL -1/2016-17	5 Lakhs	Completed
5.	Dr. Poonam Upadhyay	MHRD	Two week ISTE STTP FDP on Electric power systems	Office note (Aug-2017)	0.9 Lakhs	Completed

S.No	Name of the faculty member/s	Funded R&D Projects AY-2016-17				
		Name of the Agency	Title of the Project	File Number	Amount Sanctioned	Status of the project report

1.	Dr. N. Krishna Kumari	UGC	FPGA Implementation of Field Oriented Control for Permanent Magnet Synchronous Motor	MRP-5423/14 (SERO/UGC)	3.29 Lakhs	Completed
2.	Mr. G. Sasi Kumar	UGC	Hardware implementation of FPGA based controller for multilevel inverter	MRP-5423/14 (SERO/UGC)	2.37 Lakhs	Completed
3.	Mrs. S. Poornima	UGC	Development and implementation of DSP B algorithm for the protection of power TR	MRP-5423/14 (SERO/UGC)	2.5 Lakhs	Completed
4.	Mr. D.S.G. Krishna	UGC	Design & Development of Maximum power tracking DC link & static converter for smart micro grid application	MRP-5423/14 (SERO/UGC)	2.55 Lakhs	Completed
		FIST, DST	Development of Advanced Digital Signal Processing & Drives	SR/FST/College-148/2013(C)	11.1 Lakhs	On-going

S.No	Name of the faculty member/s	Funded R&D Projects AY-2015-16				
		Name of the Agency	Title of the Project	File Number	Amount Sanctioned	Status of the project report
1.	Dr. N. Krishna Kumari	UGC	FPGA Implementation of Field Oriented Control for Permanent Magnet Synchronous Motor	MRP-5423/14 (SERO/UGC)	3.29 Lakhs	On-going
2.	Mr. G. Sasi Kumar	UGC	Hardware implementation of FPGA based controller for multilevel inverter	MRP-5423/14 (SERO/UGC)	2.37 Lakhs	On-going
3.	Dr. D.Ravi Kumar	UGC	Development of Optimization Techniques for Protective Devices and Distributed Generators Allocation to Optimize Reliability and to Reduce Losses in	MRP-4562/42/42(SERO/UGC)	4.9 Lakhs	Completed

			Electrical Power Distribution System			
4.	Mrs. S. Poornima	UGC	Development and implementation of DSP B algorithm for the protection of power TR	MRP-5423/14 (SERO/UGC)	2.5 Lakhs	On-going
5.	Mr. D.S.G. Krishna	UGC	Design & Development of Maximum power tracking DC link & static converter for smart micro grid application	MRP-5423/14 (SERO/UGC)	2.55 Lakhs	On-going
		FIST, DST	Development of Advanced Digital Signal Processing & Drives	SR/FST/College-148/2013(C)	11.1 Lakhs	On-going

Sponsored Research (Ongoing projects)

S.N	Title of the Project	Funding Agency	Investigators (1. Details of Principal investigators, 2. Details of Co-Investigators)		Project Duration & Period	Sanctioned Amount (Lacs)	Amount Received in CAY (Lacs)	Total Amount Received till date (Lacs)
			Name	Designation				
1.	Development of Low Cost-Efficient Charging Station for Electric Vehicle (EV) Charging Applications	TIASN, SEED, DST	1. Dr. Naresh Pasula 2. Mr. D.S.G. Krishna	Assistant Professor	3 Years	10.3	-	Received mail from DST that proposal recommended for funding. Amount not mentioned in mail.
2.	Development of Advanced Digital Signal Processing & Drives	FIST, DST	Mr. D.S.G. Krishna	Assistant Professor	5 Years (2015-20)	11.1	-	11.1
3.	Data acquisition based control design of regular and special electrical machines under	AICTE	1. Dr. K. Anuradha 2. Mr. G.C.	Professor	2 Years (2017-19)	12	-	12

	MODROBS		Prabhakar				
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Funded R & D projects

Prior to the accreditation visit

S.No	Name of the faculty member/s	Funded R&D Projects AY-2014-15				
		Name of the Agency	Title of the Project	File Number	Amount Sanctioned	Status of the project report
1.	Dr. D.Ravi Kumar	UGC	Development of Optimization Techniques for Protective Devices and Distributed Generators Allocation to Optimize Reliability and to Reduce Losses in Electrical Power Distribution System	MRP-4562/42/42(SERO/UGC)	4.9 Lakhs	On-going
2.	Mr. D.S.G. Krishna	FIST, DST	Development of Advanced Digital Signal Processing & Drives	SR/FST/College-148/2013(C)	11.1 Lakhs	On-going

S.No	Name of the faculty member/s	Funded R&D Projects AY-2013-14				
		Name of the Agency	Title of the Project	File Number	Amount Sanctioned	Status of the project report
1.	Mr. P. Ramesh	VNR VJIET	Design of Pick and place mobile Robot and IR sensor based mobile Robot for tracking	Office note	0.18 Lakhs	Completed

			applications			
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S.No	Name of the faculty member/s	Funded R&D Projects AY-2012-13				
		Name of the Agency	Title of the Project	File Number	Amount Sanctioned	Status of the project report
1.	Dr. K. Anuradha and Dr. Y. Venu	AICTE	Modern Control Techniques for Power Systems	RFID/SDP/241/201 1-12	6.56 Lakhs	Completed